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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITING PROCEDURE
EXAMINING GROUP 2800

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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|---|---|---|---|
| Applicant: | Lin, *et al.* | Docket No.: | TSM03-0499 |
| Serial No.: | 10/678,710 | Art Unit: | 2825 |
| Filed: | October 3, 2003 | Examiner: | Rocchegiani, R. |
| For: | Dual Fully-Silicided Gate MOSFETs | | |

Mail Stop: After Final
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 CFR § 1.116**

Dear Sir:

Applicants respectfully submit the following amendments and remarks in response to Examiner's Office Action dated September 21, 2004, which Action is made Final. Applicants respectfully request that these amendments and remarks be entered in pursuant to the provisions of 37 CFR § 1.116, and respectfully request reconsideration of Claims 1-41.

Please Enter
the amendments

B. V. Keshavan
Examiner
01/05/05